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запитом «става від» .  
Category:Audio software plugins  
for LinuxThis invention relates to a

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method for forming a pattern of line-and-space wiring on the surface of an insulating substrate, and an apparatus therefor. As a result of the recent progress in the development of an LSI manufacturing process, a step of patterning a wiring has become an important step. Since a wiring requires conductivity, a connection hole is generally formed in a substrate so as to connect the

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wiring with a conductive region, such as a region of a conductive thin film, of the substrate.

Therefore, a method for forming a pattern of line-and-space wiring on the surface of an insulating substrate is known which comprises the steps of forming a conductive film on the surface of the substrate and removing portions of the conductive film so as to connect the conductive film to a



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conductive region of the substrate.  
For example, Japanese Patent  
Publication No. 63-53427  
(Japanese Laid-Open Patent  
Application No. 61-238254)  
discloses a method which  
comprises the steps of forming a  
wiring pattern comprising at least  
one of a conductive film and an  
insulating film, on the surface of a  
glass substrate, carrying out a resist  
coating on the surface of the

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substrate, exposing a part of the surface of the substrate through a photomask to light having a prescribed pattern, developing the resist to form a resist pattern, etching the substrate in accordance with the resist pattern to form a hole in the insulating substrate, depositing a conductive film on the surface of the substrate including the hole, and patterning the conductive film to form a wiring

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connected to the conductive region in the hole. Japanese Patent Publication No. 63-53427 does not disclose a step of patterning the conductive film by electro-deposition of the conductive film or by etching. In order 2d92ce491b